

AMENDMENTS TO THE SPECIFICATION

- (1) Please replace the paragraph beginning "The signal processing circuit" on page 8 with the following amended paragraph:

The signal processing circuit 3x is placed to sample and hold an output voltage Vsx from the C-V conversion circuit 2x (i.e., the output of the operational amplifier 21) at intervals in synchronism with the square waves P1x and P2x. In this circuit 3x, signals resulting from the sample-and-hold processing ~~is then are then~~ subjected to predetermined signal processing to output a detection signal OUTx that depends on an X-axis directional acceleration acting on the movable electrode 12 of the sensor element 10x. In order to have such operations, the signal processing circuit 3x is provided with two sample-and-hold circuits 4x and 5x, a differential amplifier circuit 6x, and a low-pass filter (LPF) 7x. The differential amplifier circuit 6x applies differential amplification to outputs from the two sample-and-hold circuits 4x and 5x.

- (2) Please replace the paragraph beginning "In addition to the above" on page 10 with the following amended paragraph:

In addition to the above configurations, the acceleration detecting apparatus according to the present embodiment is provided with a control circuit 8. This control circuit 8 includes, as its essential component, a microcomputer 8A. Hence, based on a software-based operation of the microcomputer (CPU) 8A allows the control circuit 8 to provide the foregoing square waves P1x, P2x, P1y and P2y and the switch signals SRx, S1x, S2x, SRy, S1y and S2y at timings shown Fig. 2. The present embodiment employs a manner of switching on the corresponding switches when the switch signals SRx, S1x, S2x, SRy, S1y and S2y ~~becomes become~~ high in level. The control circuit 8 may be produced, if necessary, by using digital logic circuits.